

Information Disclosure Statement By Applicant

Atty Docket No. NWISP043

Application No.: 10/635,793

Applicant:

Nemawarkar et al.

Group

(Use Several Sheets if Necessary)

Filing Date 08-05-2003

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U.S. Patent Documents

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Tran	slation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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Other Documents

Examiner		
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
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Examiner		Date Considered 4/16/06
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified)

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Examiner		Document	Publication	Country or		Sub-	Trans	lation
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DT	B2	EP0978781	2/9/00	EPO			X	

Other Documents

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Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication					
DT	C1	Copy of International Search Report dated July 30, 2004, from PCT Application No. PCT/US2003/034687, 9 pages (NWISP015.WO)					
25	C2	D. E. Culler, J. P. Singh, A. Gupta, "Parallel Computer Architecture", 1999 Morgan Kaufmann, San Francisco, CA USA XP002277658					
07	C3	Andrew Tanenbaum, "Computer Networks", Computer Networks, London: Prentice Hall International, GB, 1996, pp. 345-403, XP002155220					
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Examiner		Date Considered /					
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